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Substitute for form 1449A/PTO <h1>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h1> <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/534,304
				Filing Date	May 9, 2005
				First Named Inventor	Wolfgang EINBRODT
				Art Unit	
				Examiner Name	
				Attorney Docket Number	D4695-00133
Sheet	1	of	2		

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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
	-----	PCT Int'l Search Report			for information only	
	-----	German Search Report			for information only	
		JP09148617A2	06-06-1997	Satoshi		
		JP63174358A2	07-18-1988	Kayao		
		WO 02/067339A1	08-29-2002	Yang, et. al.		
		WO 02/49120A1	06-20-2002	Hall, et. al.		
		WO 02/33755A1	04-25-2002	Augusto, et. al.		
Examiner Signature				Date Considered		

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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Sheet

2

2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher city and/or country where published	T ²
		FOERTSCH, M.; "220 MHz optical receiver with large-area integrated PIN photodiode". Proceedings of IEEE Sensors 2003; Bd. Vol. 2 of 2, Conf. 2.	
		FOERTSCH, M.; "Integrated PIN photodiodes in high-performance BiCMOS technology". Int'l Electron Devices Meeting 2002.	
		HOHENBILD, M.; "Advanced photodiodes and circuits for OPTO-ASICs". 2001 International Symposium on Electron Devices for Microwave and Optoelectronic Devices.	
		KUCHTA, D.; "Performance of fiber-optic data links using 670-nm cw VCSELs and a monolithic Si photodetector and CMOS preamplifier". IBM Jnl. Res. Develop. 39, pp. 63-72, 1995.	
		KYOMASU, M.; "Development of an integrated high speed silicon PIN photodiode sensor". IEEE Trans. On Electron Dev., vol. 42, no. 6 pp. 1093-1099, June 1995.	
		LIM, P.; "A 3.3-V monolithic photodetector/CMOS preamplifier for 532 Mb/s optical data link applications". Digest Technical Papers ISSCC 1993, pp. 96-97.	
		YAMAMOTO, M.; "Si-OEIC with aq built-in pin-photodiode". IEEE Trans. Electron Dev. 42 (1), pp. 58-63, 1995.	
		YANG, M.; "A high-speed, high sensitivity silicon lateral trench photodetector". IEEE Electron Dev. Lett., pp. 395-397, 2002.	
		ZIMMERMAN, H.; "Monolithic high-speed CMOS-photoreceiver". IEEE Photonics Technology Letters 11, pp. 254-256, 1999.	
		ZIMMERMAN, H.; "Monolithic bipolar-, CMOS-, and BiCMOS-receiver OEICs". 1996 International Semiconductor Conference; Bd. Vol. 2, Conf. 19.	

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